

L Number	Hits	Search Text	DB	Time stamp
2	1	6597808.pn. and manhattan	USPAT	2004/04/25 12:26
3	1	5559997.pn. and (crosstalk or (cross adj talk))	USPAT	2004/04/25 14:42
4	0	5559997.pn. and gap	USPAT	2004/04/25 14:42
6	0	5559997.pn. and spac\$3	USPAT	2004/04/25 14:42
5	1	5559997.pn. and pattern	USPAT	2004/04/25 14:42
15	3025421	@ad<20000511	USPAT; IBM_TDB	2004/04/25 15:36
16	472	703/14.cccls.	USPAT; IBM_TDB	2004/04/25 15:36
17	345	703/1.cccls.	USPAT; IBM_TDB	2004/04/25 15:36
18	620	716/1.cccls.	USPAT; IBM_TDB	2004/04/25 15:36
19	1371	703/14.cccls. or 703/1.cccls. or 716/1.cccls.	USPAT; IBM_TDB	2004/04/25 15:37
21	1069	@ad<20000511 and (703/14.cccls. or 703/1.cccls. or 716/1.cccls.)	USPAT; IBM_TDB	2004/04/25 15:37
22	149	(@ad<20000511 and (703/14.cccls. or 703/1.cccls. or 716/1.cccls.)) and noise	USPAT; IBM_TDB	2004/04/25 15:37
23	1	6493658.pn.	USPAT	2004/04/25 16:18
26	467	382/173.cccls.	USPAT	2004/04/25 16:16
27	417	@ad<20000511 and 382/173.cccls.	USPAT	2004/04/25 16:16
28	208	(@ad<20000511 and 382/173.cccls.) and noise	USPAT	2004/04/25 16:16
1	1	6597808.pn.	USPAT	2004/04/25 16:26
29	1	6493658.pn.	USPAT	2004/04/25 16:18
30	1	6597808.pn.	USPAT	2004/04/25 16:26
-	3024994	@ad<20000511	USPAT; IBM_TDB	2004/04/25 15:36
-	3024994	@ad<20000511	USPAT	2004/04/18 14:22
-	290500	noise	USPAT	2004/04/18 14:22
-	2142028	reduc\$5 or minimiz\$5	USPAT	2004/04/18 14:23
-	65919	noise near2 (reduc\$5 or minimiz\$5)	USPAT	2004/04/18 14:24
-	45167	noise near1 (reduc\$5 or minimiz\$5)	USPAT	2004/04/18 14:26
-	19480	noise adj (reduc\$5 or minimiz\$5)	USPAT	2004/04/18 14:27
-	15476	countermeasure	USPAT	2004/04/18 14:27
-	661	(noise near1 (reduc\$5 or minimiz\$5)) and countermeasure	USPAT	2004/04/18 14:27
-	475	(noise near1 (reduc\$5 or minimiz\$5)) and countermeasure and circuit\$	USPAT	2004/04/18 14:28
-	57	(noise near1 (reduc\$5 or minimiz\$5)) and countermeasure and circuit\$ and simulat\$5	USPAT	2004/04/18 14:28
-	48	@ad<20000511 and ((noise near1 (reduc\$5 or minimiz\$5)) and countermeasure and circuit\$ and simulat\$5)	USPAT	2004/04/18 14:43
-	248	manhattan adj distance	USPAT	2004/04/18 14:43
-	215	@ad<20000511 and (manhattan adj distance)	USPAT	2004/04/19 12:25
-	1	5559997.pn.	USPAT	2004/04/25 14:18
-	822	fujimori.in.	USPAT	2004/04/25 15:45
-	66	fujimori-\$\$.in.	USPAT	2004/04/23 16:37
-	1	fujimori-shogo\$.in.	USPAT	2004/04/23 16:39
-	16	Yamashita-Yasuhiro\$.in.	USPAT	2004/04/25 12:25

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Leach, W.M., Jr.;
Proceedings of the IEEE , Volume: 82 , Issue: 10 , Oct. 1994
Pages:1515 - 1538

[\[Abstract\]](#) [\[PDF Full-Text \(1960 KB\)\]](#) IEEE JNL**2 Analog circuit design in scaled CMOS technology**

Sansen, W.;
VLSI Circuits, 1996. Digest of Technical Papers., 1996 Symposium on , 13-15 1996
Pages:8 - 11

[\[Abstract\]](#) [\[PDF Full-Text \(376 KB\)\]](#) IEEE CNF**3 Developments in RF circuit design**

Haigh, D.; Webster, D.; Kaunisto, R.; Ndulijubd, C.; Khanifar, A.; Darvishzadeh, M.; Parker, T.; Scott, J.; Thayne, I.;
RF Design Scene, IEE Colloquium on , 15 Feb 1996
Pages:4/1 - 410

[\[Abstract\]](#) [\[PDF Full-Text \(808 KB\)\]](#) IEE CNF**4 Noise-tolerant dynamic circuit design**

Lei Wang; Shanbhag, N.R.;
Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on , Volume: 1 , 30 May-2 June 1999
Pages:549 - 552 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(372 KB\)\]](#) IEEE CNF

5 Energy-efficient dynamic circuit design in the presence of crosstalk

Balamurugan, G.; Shanbhag, N.R.;

Low Power Electronics and Design, 1999. Proceedings. 1999 International Symposium on , 16-17 Aug. 1999

Pages:24 - 29

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) [IEEE CNF](#)

6 Low switching noise CMOS circuit design strategy based on regular timed structures

Gonzalez, J.L.; Rubio, A.;

Circuits and Systems, 1998. Proceedings. 1998 Midwest Symposium on , 9-11 Aug. 1998

Pages:176 - 179

[\[Abstract\]](#) [\[PDF Full-Text \(736 KB\)\]](#) [IEEE CNF](#)

7 A crosstalk tolerant latch circuit design

Rubio, A.; Pons, J.; Anglada, R.;

Circuits and Systems, 1990., Proceedings of the 33rd Midwest Symposium on 14 Aug. 1990

Pages:653 - 656 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(228 KB\)\]](#) [IEEE CNF](#)

8 Application of small-signal model to DC SQUID circuit design

Geren, W.P.;

Applied Superconductivity, IEEE Transactions on , Volume: 5 , Issue: 2 , Jun

Pages:2770 - 2773

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) [IEEE JNL](#)

9 Application specific CMOS output driver circuit design techniques to reduce simultaneous switching noise

Senthinathan, R.; Prince, J.L.;

Solid-State Circuits, IEEE Journal of , Volume: 28 , Issue: 12 , Dec. 1993

Pages:1383 - 1388

[\[Abstract\]](#) [\[PDF Full-Text \(440 KB\)\]](#) [IEEE JNL](#)

10 Unified 1/f noise SOI MOSFET modelling for circuit simulation

Iniguez, B.; Tambani, M.; Dessard, V.; Flandre, D.;

Electronics Letters , Volume: 33 , Issue: 21 , 9 Oct. 1997

Pages:1781 - 1782

[\[Abstract\]](#) [\[PDF Full-Text \(212 KB\)\]](#) [IEE JNL](#)

11 Noise measurements on Power4 Test chip

Haridass, A.; James, N.; McCredie, B.;

Electrical Performance of Electronic Packaging, 1999 , 25-27 Oct. 1999

Pages:25 - 28

[\[Abstract\]](#) [\[PDF Full-Text \(272 KB\)\]](#) [IEEE CNF](#)

12 1999 Southwest Symposium on Mixed-Signal Design (Cat. No.99EX)

Mixed-Signal Design, 1999. SSMSD '99. 1999 Southwest Symposium on , 11- April 1999

[\[Abstract\]](#) [\[PDF Full-Text \(308 KB\)\]](#) [IEEE CNF](#)

13 Design of front-end RF circuits

Gharpurey, R.; Viswanathan, T.R.;

Mixed-Signal Design, 1999. SSMSD '99. 1999 Southwest Symposium on , 11- April 1999

Pages:134 - 139

[\[Abstract\]](#) [\[PDF Full-Text \(496 KB\)\]](#) [IEEE CNF](#)

14 A physically based model for low-frequency noise of poly-silicon resistors

Brederlow, R.; Weber, W.; Dahl, C.; Schmitt-Landsiedel, D.; Thewes, R.; Electron Devices Meeting, 1998. IEDM '98 Technical Digest., International , 6 Dec. 1998

Pages:89 - 92

[\[Abstract\]](#) [\[PDF Full-Text \(308 KB\)\]](#) [IEEE CNF](#)

15 A fully monolithic HMIC low noise amplifier

Boles, T.;

Radio Frequency Integrated Circuits (RFIC) Symposium, 1997., IEEE , 8-11 Ju 1997

Pages:109 - 112

[\[Abstract\]](#) [\[PDF Full-Text \(340 KB\)\]](#) [IEEE CNF](#)

[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [11](#) [12](#) [13](#) [14](#) [15](#) [16](#) [17](#) [18](#) [19](#) [20](#) [21](#) [22](#) [23](#)
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1 A feedback control circuit design technique to suppress power noise in high speed output driver

Choy, C.S.; Chan, C.F.; Ku, M.H.;

Circuits and Systems, 1995. ISCAS '95., 1995 IEEE International Symposium on , Volume: 1 , 28 April-3 May 1995

Pages:307 - 310 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(340 KB\)\]](#) **IEEE CNF**

2 Harmony: static noise analysis of deep submicron digital integrated circuits

Shepard, K.L.; Narayanan, V.; Rose, R.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction on , Volume: 18 , Issue: 8 , Aug. 1999

Pages:1132 - 1150

[\[Abstract\]](#) [\[PDF Full-Text \(404 KB\)\]](#) **IEEE JNL**

3 Substrate noise in mixed signal circuits: two case studies [CMOS]

England, D.;

ASIC Conference 1998. Proceedings. Eleventh Annual IEEE International , 13- Sept. 1998

Pages:37 - 41

[\[Abstract\]](#) [\[PDF Full-Text \(392 KB\)\]](#) **IEEE CNF**

4 A model to hardware comparison of simultaneous switching noise on CMOS chip

McCredie, B.; Kuppingen, S.; Katopis, G.; Becker, W.D.;

Electrical Performance of Electronic Packaging, 1994., IEEE 3rd Topical Meeting

on , 2-4 Nov. 1994

Pages:43 - 45

[Abstract] [PDF Full-Text (172 KB)] IEEE CNF

5 Ground noise estimation and minimization in integrated circuit pac

Williamson, J.; Kalaichelvan, K.; Nakhla, M.; Zhang, Q.J.; van der Puije, P.; Electromagnetic Compatibility, 1993. Symposium Record. 1993 IEEE Internat Symposium on , 9-13 Aug. 1993

Pages:425 - 428

[Abstract] [PDF Full-Text (292 KB)] IEEE CNF

6 Modeling of power/ground plane noise in high speed digital electro

packaging

Fang, J.; Liu, Y.; Chen, Y.; Wu, Z.; Agrawal, A.; Electrical Performance of Electronic Packaging, 1993 , 20-22 Oct. 1993

Pages:206 - 208

[Abstract] [PDF Full-Text (184 KB)] IEEE CNF

7 A new efficient transient noise analysis technique for simulation of

image sensors or particle detectors

Bolcato, P.; Tawfik, M.S.; Poujols, R.; Jarron, P.;

Custom Integrated Circuits Conference, 1993., Proceedings of the IEEE 1993 May 1993

Pages:14.8.1 - 14.8.4

[Abstract] [PDF Full-Text (340 KB)] IEEE CNF

8 Substrate noise measurement by using noise-selective voltage

comparators in analog and digital mixed-signal integrated circuits

Makie-Fukuda, K.; Anbo, T.; Tsukada, T.;

Instrumentation and Measurement, IEEE Transactions on , Volume: 48 , Issue: 6 , Dec. 1999

Pages:1068 - 1072

[Abstract] [PDF Full-Text (132 KB)] IEEE JNL

9 Hybrid infrared focal plane signal and noise model

Johnson, J.F.;

Electron Devices, IEEE Transactions on , Volume: 46 , Issue: 1 , Jan. 1999

Pages:96 - 108

[Abstract] [PDF Full-Text (604 KB)] IEEE JNL

10 Measurement of digital noise in mixed-signal integrated circuits

Makie-Fukuda, K.; Kikuchi, T.; Matsuura, T.; Hotta, M.;

Solid-State Circuits, IEEE Journal of , Volume: 30 , Issue: 2 , Feb. 1995

Pages:87 - 92

[Abstract] [PDF Full-Text (484 KB)] IEEE JNL

11 On-chip active guard band filters to suppress substrate-coupling noise in analog and digital mixed-signal integrated circuits*Makie-Fukuda, K.; Tsukada, T.;*VLSI Circuits, 1999. Digest of Technical Papers. 1999 Symposium on , 17-19
1999

Pages:57 - 60

[\[Abstract\]](#) [\[PDF Full-Text \(328 KB\)\]](#) [IEEE CNF](#)**12 Resistance transients in thin-film noise data [IC interconnects]***Head, L.M.;*Integrated Reliability Workshop Final Report, 1997 IEEE International , 13-16
1997

Pages:7 - 12

[\[Abstract\]](#) [\[PDF Full-Text \(560 KB\)\]](#) [IEEE CNF](#)**13 High density integrated circuit design: simultaneous switching
ground/power noises calculation for pin grid array packages***Bedouani, M.;*Electronic Components and Technology Conference, 1993. Proceedings., 43rd
June 1993

Pages:1039 - 1044

[\[Abstract\]](#) [\[PDF Full-Text \(332 KB\)\]](#) [IEEE CNF](#)**14 Modeling digital substrate noise injection in mixed-signal IC's***Charbon, E.; Miliozzi, P.; Carloni, L.P.; Ferrari, A.; Sangiovanni-Vincentelli, A.*
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction
on , Volume: 18 , Issue: 3 , March 1999

Pages:301 - 310

[\[Abstract\]](#) [\[PDF Full-Text \(228 KB\)\]](#) [IEEE JNL](#)**15 Characterization of crosstalk noise in submicron CMOS integrated
circuits: an experimental view***Fourniols, J.-Y.; Roca, M.; Caignet, F.; Sicard, E.;*Electromagnetic Compatibility, IEEE Transactions on , Volume: 40 , Issue: 3 ,
1998

Pages:271 - 280

[\[Abstract\]](#) [\[PDF Full-Text \(300 KB\)\]](#) [IEEE JNL](#)[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [11](#) [12](#) [13](#) [14](#) [15](#) [16](#) [17](#) [18](#) [19](#) [20](#) [21](#) [22](#) [23](#)
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